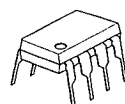


SINGLE-SUPPLY DUAL COMPARATOR

■ GENERAL DESCRIPTION

The NJM2903/2403 consist of two independent precision voltage comparators with an offset voltage specification as low as 5.0mV max for two comparators which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. The NJM2903/2403 has a unique characteristics: the input common-mode voltage range includes ground, even though operated from a single power supply voltage. Application areas include limit comparators, simple analog-to-digital converters; pulse, square-wave and time delay generators; wide range Vco; MOS clock timers; multivibrators and high voltage digital logic gates. The NJM2903/2403 were designed to directly interface with TTL and CMOS. When operated from both plus and minus power supplies, the NJM2903/2403 will directly interface with MOS logic where their low power drain is a distinct advantage over standard comparators.

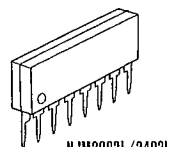
■ PACKAGE OUTLINE



NJM2903D/2403D



NJM2903M/2403M



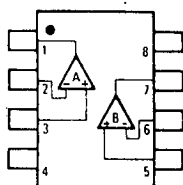
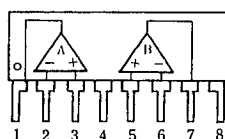
NJM2903L/2403L

NJM2903V
NJM2403V

■ FEATURES

- Operating Voltage (+2V ~ +36V)
- Single Supply Operation
- Open Collector Output
- High Output Sink Current (15mA @2403)
- Package Outline DIP8, DMP8, SIP8, (SSOP8)
- Bipolar Technology

■ PIN CONFIGURATION

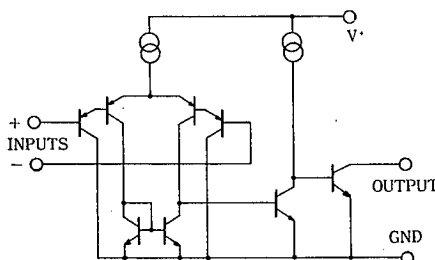
NJM2903D/2403D
NJM2903M/2403M
NJM2903V/2403V

NJM2903L/2403L

PIN FUNCTION

1. A OUTPUT
2. A-INPUT
3. A+INPUT
4. GND
5. B+INPUT
6. B-INPUT
7. B OUTPUT
8. V⁺

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V*	36(or ±18)	V
Differential Input Voltage	V _{ID}	36	V
Input Voltage	V _{IN}	-0.3~+36	V
Power Dissipation	P _D	(DIP8) 500	mW
		(DMP8) 300	mW
		(SSOP8) 250	mW
		(SIP8) 800	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-50~+125	°C

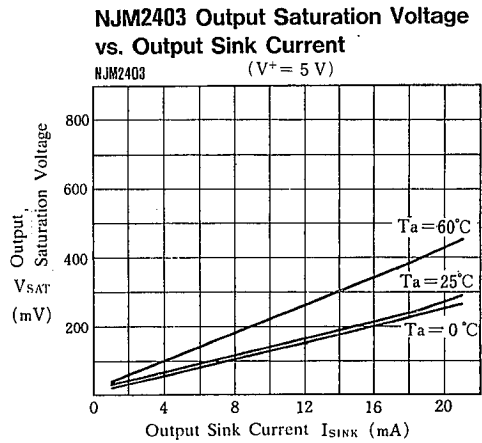
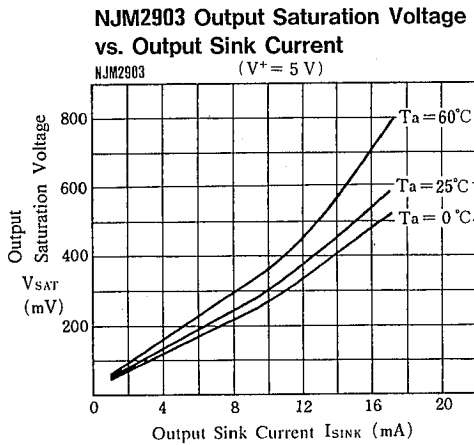
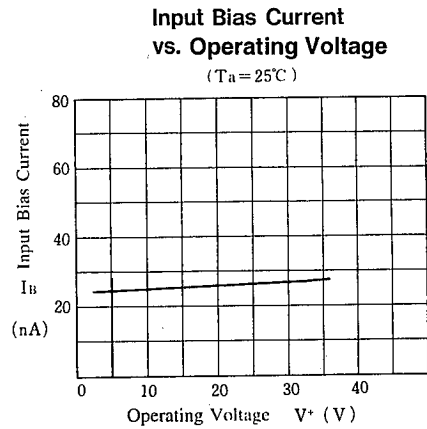
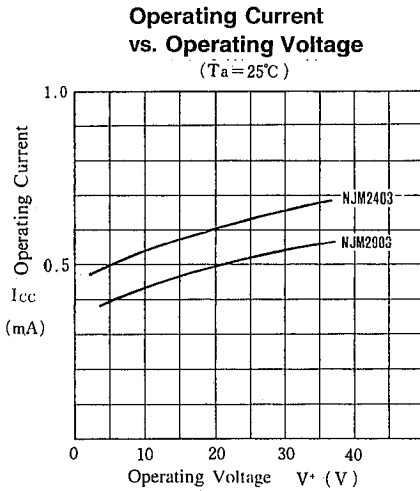
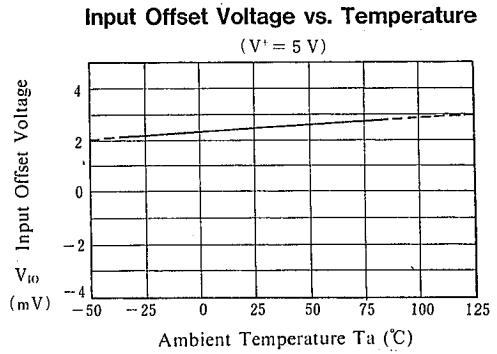
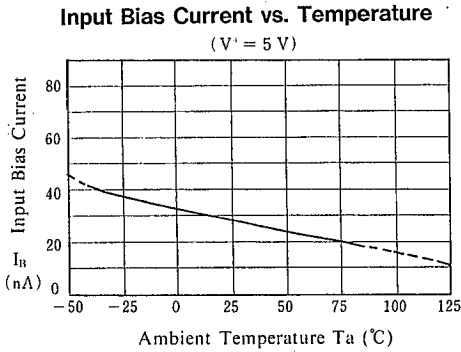
■ ELECTRICAL CHARACTERISTICS

(V*=5V, Ta=25°C)

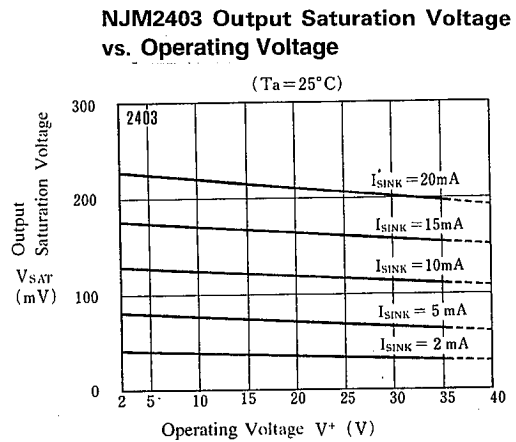
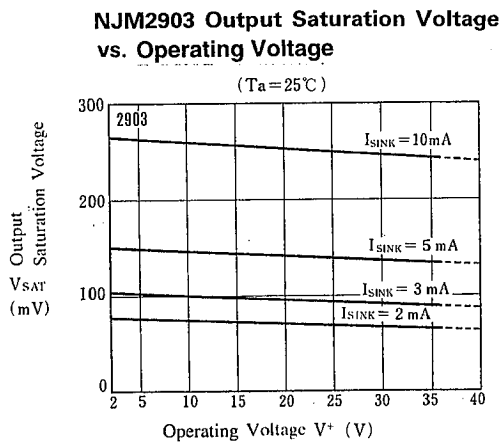
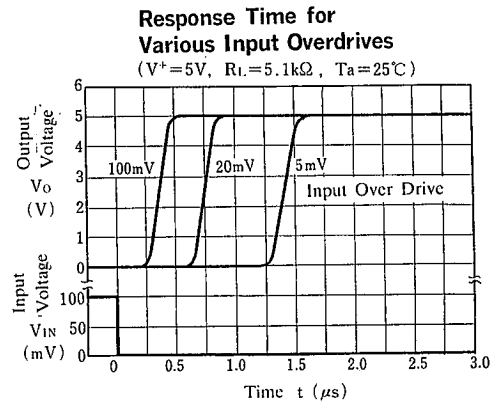
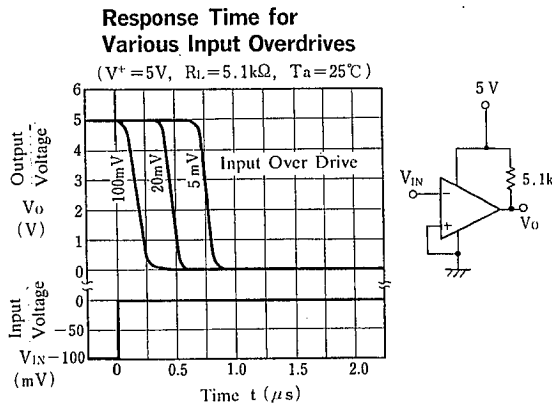
PARAMETER	SYMBOL	TEST CONDITION	2903			2403			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Input Offset Voltage	V _{IO}	R _S =0Ω, V _O ≅1.4V	—	—	7	—	—	10	mV
Input Offset Current	I _{IO}		—	—	50	—	—	100	nA
Input Bias Current	I _B		—	30	250	—	40	500	nA
Input Common Mode Voltage Range	V _{ICM}		0~3.5	—	—	0~3.5	—	—	V
Large Signal Voltage Gain	A _V	R _L =15kΩ	—	106	—	—	106	—	dB
Response Time	t _R	R _L 5.1kΩ	—	1.5	—	—	1.5	—	μS
Output Sink Current	I _{SINK}	V _{IN} ⁻ =1V, V _{IN} ⁺ =0V, V _O =1.5V	6	—	—	20	—	—	mA
Output Saturation Voltage	V _{SAT}	V _{IN} ⁻ =1V, V _{IN} ⁺ =0V _m I _{SINK} =3mA	—	200	400	—	—	—	mV
Output Saturation Voltage	V _{SAT}	V _{IN} ⁻ =1V, V _{IN} ⁺ =0V, I _{SINK} =15mA	—	—	—	—	200	400	mV
Output Leakage Current	I _{LEAK}	V _{IN} ⁻ =0V, V _{IN} ⁺ =0V, V _O =5V	—	—	1.0	—	—	1.0	μA
Operating Current	I _{CC}		—	0.4	1.0	—	0.5	1.5	mA

5

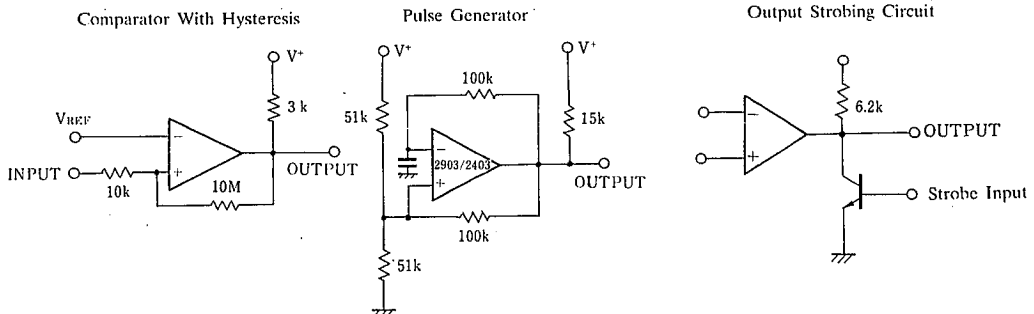
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS



■ TYPICAL APPLICATIONS



MEMO

[CAUTION]

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