## 8-CHANNEL SATURATED SINK DRIVER

Dwg. No. A-11,407

#### **ABSOLUTE MAXIMUM RATINGS**

at 25°C Free-Air Temperature for any one driver (unless otherwise noted)

Output Voltage, V <sub>CE</sub> 20 V
Supply Voltage, V <sub>S</sub> 20 V
Input Voltage, $V_{IN}$ 20 V
Output Current, l <sub>C</sub> <b>200 mA</b>
Ground Terminal Current, $I_{GND}$ 1.6 A
Package Power Dissipation, P <sub>D</sub> See Graph
Operating Temperature Range, $T_{A}$ -20°C to +85°C
Storage Temperature Range, T <sub>S</sub> 55°C to +150°C

Developed for use with low-voltage LED and incandescent displays requiring low output saturation voltage, the UDN2595A and A2595SLW meet many interface needs, including those exceeding the capabilities of standard logic buffers. The eight non-Darlington outputs of each driver can continuously and simultaneously sink load currents of 100 mA at ambient temperatures of up to +75°C.

The eight-channel driver's active-low inputs can be driven directly from TTL, Schottky TTL, DTL, 5 to 16 V CMOS, and NMOS logic. All input connections are on one side of the package, output connections on the other, for simplified printed wiring board layouts.

These drivers are packaged in plastic DIPs (suffix A) or surface-mountable wide-body SOlCs (suffix LW), and are rated for operation over the temperature range of -20°C to +85°C. The A2595SLW is also available for operation to -40°C. To order, change the suffix from 'SLW' to 'ELW'.

#### **FEATURES**

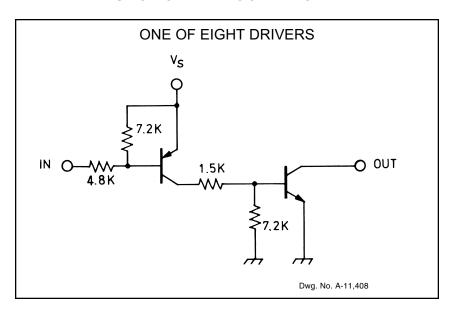
- Non-Inverting Function (Input Low = Output ON)
- 200 mA Current Rating
- 100 mA Continuous and Simultaneous (All outputs) to +85°C
- Low Saturation Voltage
- TTL, CMOS, NMOS Compatible
- Efficient Input/Output Pin Format
- DIP or SOIC Packaging

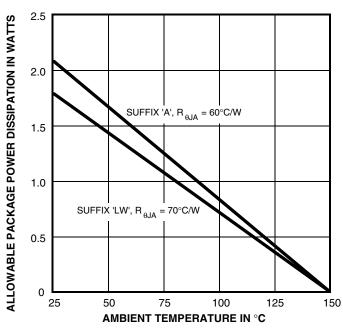
Always order by complete part number:

Part Number	Package			
UDN2595A	18-Pin DIP			
A2595SLW	20-Lead Wide-Body SOIC			

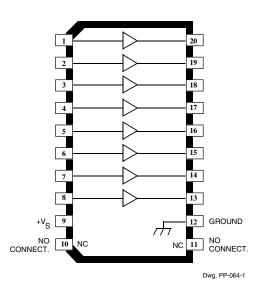


#### **FUNCTIONAL BLOCK DIAGRAM**





#### A2595SLW



Dwg. GS-009-1



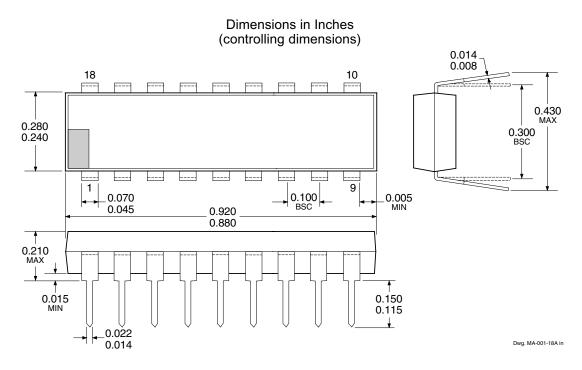
### ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ , $V_S = 5.0 \text{ V}$ (unless otherwise noted).

			Limits		
Characteristic	Symbol	Test Conditions	Min.	Max.	Units
Output Leakage	I <sub>CEX</sub>	V <sub>IN</sub> ≥ 4.5 V, V <sub>OUT</sub> = 20 V, T <sub>A</sub> = 25°C	_	50	μΑ
Current		V <sub>IN</sub> ≥ 4.6 V, V <sub>OUT</sub> = 20 V, T <sub>A</sub> = 70°C	_	100	μΑ
Output Saturation	V <sub>CE(SAT)</sub>	V <sub>IN</sub> = 0.4 V, I <sub>OUT</sub> = 50 mA	_	0.5	V
Voltage	, ,	V <sub>IN</sub> = 0.4 V, I <sub>OUT</sub> = 100 mA	_	0.6	V
Input Current	I <sub>IN(ON)</sub>	V <sub>IN</sub> = 0.4 V, I <sub>OUT</sub> = 100 mA	_	-1.6	mA
		$V_{IN}$ = 0.4 V, $I_{OUT}$ = 100 mA, $V_{S}$ = 15 V	_	-5.0	mA
Input Voltage	V <sub>IN(ON)</sub>	I <sub>OUT</sub> = 100 mA, V <sub>OUT</sub> ≤ 0.6 V	_	0.4	V
	V <sub>IN(OFF)</sub>	I <sub>OUT</sub> = 100 μA, T <sub>A</sub> = 70°C	4.6	_	V
Input Capacitance	C <sub>IN</sub>		_	25	pF
Supply Current	I <sub>S</sub>	V <sub>IN</sub> = 0.4 V, I <sub>OUT</sub> = 100 mA	_	6.0	mA
		V <sub>IN</sub> = 0.4 V, I <sub>OUT</sub> = 100 mA, V <sub>S</sub> = 15 V	_	20	mA

NOTES: 1. Negative current is defined as coming out of the specified device pin.

- 2. The  $V_{\text{IN}(\text{ON})}$  voltage limit guarantees a minimum output sink current per the specified conditions.
- 3. l<sub>S</sub> is measured with any one of eight drivers turned ON.

#### **UDN2595A**



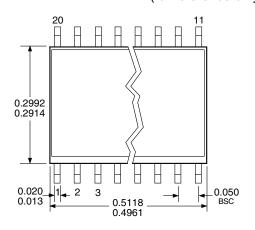
#### **Dimensions in Millimeters** (for reference only) 0.355 0.204 18 10 10.92 MAX 7.11 6.10 7.62 BSC 9 2.54 BSC 0.13 MIN 1.77 1.15 23.37 22.35 5.33 MAX 0.39 3.81 2.93 \_0.558 0.356 Dwg. MA-001-18A mm

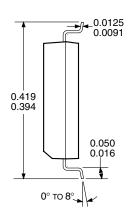
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
  - 2. Lead spacing tolerance is non-cumulative.
  - 3. Lead thickness is measured at seating plane or below.

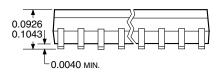


#### **A2595SLW**

# Dimensions in Inches (for reference only)

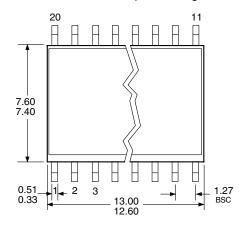


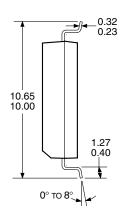


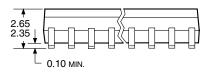


Dwg. MA-008-20 in

## Dimensions in Millimeters (controlling dimensions)







Dwg. MA-008-20 mm

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

2. Lead spacing tolerance is non-cumulative.

### 2595 8-CHANNEL SATURATED SINK DRIVER

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## **POWER SINK DRIVERS**

#### IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

Output Ratings *		Features						
		Serial	Latched	Diode		Internal	_	
mA	V	#	Input	<b>Drivers</b>	Clamp	Outputs	Protection	Part Number <sup>†</sup>
75	17	8	X	X		constant current		6275
400	17	16	X	Х		constant current		6276
100	20 30	8 32	_ X	X	-	saturated –	_	2595 5833
	40	32	x	x	_	saturated	_	5832
	50	8		essable decod	der/driver	DMOS	_	6B259
	50	8	Ξ.	X	_	DMOS	_	6B273
	50	8	X	X		DMOS	_	6B595
250	50 50	8 8		ssable decod	der/driver	DMOS	_	6259
	50 50	8 8	X	X X	_	DMOS DMOS	_	6273 6595
	135	7	_	_	Χ	-	_	7003
300	45	1	– Ha	all sensor/driv		_	Х	5140
	50	7	_	_	Χ	_	_	2003
	50	8	_	_	Х		_	2803
	50	8	_	_	X	saturated		2596
	60 95	4 7	_	_	X X	saturated	X	2557 2023
	95	8	_	_	X	_	_	2823
350	50	4	_	Х	X	_	_	5800
	50	7	_	_	Â	_	_	2004
	50	8	_	_	X X X	_	_	2804
	50	8	_	X	X	_	_	5801
	50 50	8 8	X X	X X	X	_	_	5821 5841
	50 50	8		ssable deco		DMOS	_	6A259
	50	8	X	X	_	DMOS	_	6A595
	80	8	X	X	_	_	_	5822
	80	8	Χ	X	X	_	_	5842
	95 95	<i>7</i> 8	_	_	X X	_	_	2024 2824
450	30	28	dual 4	1- to 14-line d		er –		6817
600	60	4	_	_	_	saturated	X	2547
	60	4	_	_	Χ	saturated	Â	2549
700	60	4	_	_	Χ	saturated	X	2543 and 2559
750	50	8	_	_	Χ	saturated	_	2597
900	14	2		all sensor/driv		saturated	X	3625
1000	26	2		all sensor/driv		saturated	X	3626
1000	46	4		er motor con				7024 and 7029
1200	46	4		stepping con				7042
1250	50 50	4 4		er motor tran –		r – –	X -	5804 2064 and 2068
1500	80	4			X			2065 and 2069
1800	50	4		_	X	_	_	2544
	50	4	_	_	X	_	_	2540
3000	46	4		er motor con			_	7026
	46	4	micro	stepping con		r MOS	_	7044
4000	50	4	_	_	X	_	-	2878
	80	4	_	_	Х	_	_	2879

<sup>\*</sup> Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

<sup>†</sup> Complete part number includes additional characters to indicate operating temperature range and package style.

